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<https://www.scribd.com/doc/53213826/labview-core-1>

Community: roboRIO Details and Specifications - -

Plain - roboRIO Details and Specifications - Introducing the roboRIO the next generation in student robotics controllers. This device is designed specifically with

<https://decibel.ni.com/content/docs/DOC-30419>

EasyFPGA - Altera and Xilinx FPGA development - -

EasyFPGA develops and sells Altera and Xilinx FPGA development boards with USB interface.

<http://www.easyfpga.com/>

2 Labview Programming Environment Manual PDF in -

LabVIEW Core 1 - National Instruments. IMAQ Vision for LabVIEW User Manual. 2 labview programming environment Executing Simulink models on FPGA in a LabVIEW

<http://www.manualpdfs.com/2015/2-labview-programming-environment>

FPGA Module Related Documentation - LabVIEW 2013 -

FPGA Module Related Documentation LabVIEW 2013 FPGA about programming the DSP48E1 slice on an FPGA. This manual is available on the Xilinx

http://zone.ni.com/reference/en-XX/help/371599J-01/lvfpgamain/fpga_related_documentation/

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http://www.youtube.com/watch?v=fOB_5WvXI-4

LABVIEW 2014 - Centre de Calcul de l'IN2P3 -

File: LabVIEW_2014_core Ce fichier contient les modules suivant: 1. Syst me de d veloppement professionnel LabVIEW 2014 LabVIEW 2014 FPGA Module 2.

<http://cc.in2p3.fr/docenligne/1079>

LabVIEW FPGA Design Flow And Best Practices Part -

LabVIEW FPGA Design Flow and. Best Practices Part 2 ni.com | NI Enable Chain Optimization in LabVIEW FPGA 2012. 18 ni.com | NI CONFIDENTIAL
<http://www.readezy.com/labview-fpga-design-flow-and-best-practices-part-2.pdf>

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<https://www.scribd.com/doc/207691382/177366907-2012-LabVIEW-Core-1-Exercises-Manual-PDF>

National Instruments Introduces LabVIEW FPGA IP -

National Instruments introduced LabVIEW FPGA IP Builder, which is an add-on to their LabVIEW FPGA Module. The NI LabVIEW FPGA IP Builder helps designers generate high
<http://fpgablog.com/posts/ni-xilinx-high-level-synthesis/>

LabVIEW FPGA Module - National Instruments -

LabVIEW FPGA provides IP developed by NI and Xilinx for basic functions such as counters you can debug your code with core LabVIEW debugging features such as
<http://www.ni.com/labview/fpga/>

LabVIEW tutorial - LabVIEW Wiki -

Self-paced with a manual National Instruments has a Recommended training courses for those with no knowledge of LabVIEW are the National Instruments Core 1
http://labviewwiki.org/LabVIEW_tutorial

Generating and Integrating Aurora IP into Your -

Generating and Integrating Aurora IP into Your LabVIEW core properties From the User Manual, can be debugged in LV FPGA with confidence. 10. Create LabVIEW
<http://digital.ni.com/public.nsf/allkb/723FE910AF2D01A686257D89007702C4>

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http://www.youtube.com/watch?v=yb3_gBKyrvk

LabVIEW FPGA Course Kit - National Instruments -

LabVIEW Core 1 Course Manual; LabVIEW Core 2 Course Manual; The LabVIEW FPGA Course Kit contains training materials for designing, debugging, <http://sine.ni.com/nips/cds/view/p/lang/en/nid/210336>

Field-programmable gate array - Wikipedia, the -

A field-programmable gate array (FPGA) is an integrated circuit designed to be configured by a customer or a designer after manufacturing hence "field-programmable".

http://en.wikipedia.org/wiki/Field-programmable_gate_array

NI COURSE DESCRIPTIONS | Bloomy -

LabVIEW Courses LabVIEW Core 1 LabVIEW Core 2 LabVIEW Core 3 LabVIEW Performance LabVIEW Connectivity

<http://www.bloomy.com/ni-classroom-training/ni-course-descriptions>

Spartan-6 Fpga Clocking Resources User Guide -

LabVIEW FPGA Design for Code Modules (IP Cores) Core Supply Voltage Min: 1.14V, FPGA Family: Spartan-6, I/O Supply (available at the end of the user manual)

<https://matenelaps.files.wordpress.com/2015/07/spartan-6-fpga-clocking-resources-user-guide.pdf>

Altera - Official Site -

FPGA CPLD and ASIC solutions that shorten time to market, improve performance and productivity, and reduce system costs compared to traditional DSP, ASSP, and ASIC

<https://www.altera.com/>

Community: Group: NI Training Community - National -

Create and manage categories in NI Training Community. Removing a category will not remove content. Categories in NI Training Community

<https://decibel.ni.com/content/groups/training-community?view=documents>

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<http://lv-clan.enjin.com/forum/m/6211953/viewthread/16034646-labview->

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LabVIEW Core 2 Course Manual; LabVIEW Core 3 Course Manual; LabVIEW Connectivity Course Manual; LabVIEW Real-Time 2 Course Manual; LabVIEW FPGA Course Manual;

<http://sine.ni.com/nips/cds/view/p/lang/en/nid/207901>

Manuals & Documents -

Read and Download PDF Manuals & Documents of , from and .

<http://manualsdocs.com/doc/labview-fpga-course>

fpga dsp, FFT xilinx block - Discussion Forums -

Hi at all, I'm working on LabVIEW FPGA in DSP environment and I have some questions about the FFT Xilinx block (Core gen 7.1) My clock is 130Mhz and data run with a

<http://forums.ni.com/t5/LabVIEW/fpga-dsp-FFT-xilinx-block/td-p/2502928>

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<http://www.huntsgroup.bm/beaudoin/wilton-course-1-book-free-dakotas/>

Academic Training - National Instruments US -

See step-by-step tutorials, example projects, and short videos to get started with National Instruments hardware and software. Learn the NI LabVIEW environment and

<http://us.ni.com/academic/training>